

Amendments

In the Claims:

Please substitute the following claim 11 for the pending claim 11:

A₁

11. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a transceiver.

Please substitute the following claim 12 for the pending claim 12:

A₂

12. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a computer processing system.

Please substitute the following claim 13 for the pending claim 13:

A₃

13. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the first delay device and the second delay device each comprise at least one data flip-flop.

Please substitute the following claim 14 for the pending claim 14:

A4
14. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a communications system operating at a data rate of at least 3 gigabits per second.

Please substitute the following claim 15 for the pending claim 15:

A5
15. (ONCE AMENDED) The digital logic circuit of claim 10, wherein the digital logic circuit forms part of a decision feedback equalizer.